

100
Network
Figure 1
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The diagram illustrates a computer system architecture. At the top, two processors, **Processor 202** and **Processor 204**, are connected to a horizontal **System Bus 206**. Below the system bus is a block containing the **Memory Controller/Cache 208** and the **I/O Bridge 210**. The memory controller/cache is connected to **Local Memory 209**. The I/O bridge is connected to a vertical **I/O Bus 212**. This bus connects to several components: **Graphics Adapter 230** and **Hard Disk 232** on the left; and three **PCI Bus Bridge** units (**214**, **222**, and **224**) on the right. Each PCI bus bridge is connected to a corresponding **PCI Bus** (**216**, **226**, and **228** respectively). The PCI bus 216 is connected to a **Modem 218** and a **Network Adapter 220**. The PCI bus 226 is shown with a dashed line, and the PCI bus 228 is shown with a solid line.

Figure 2

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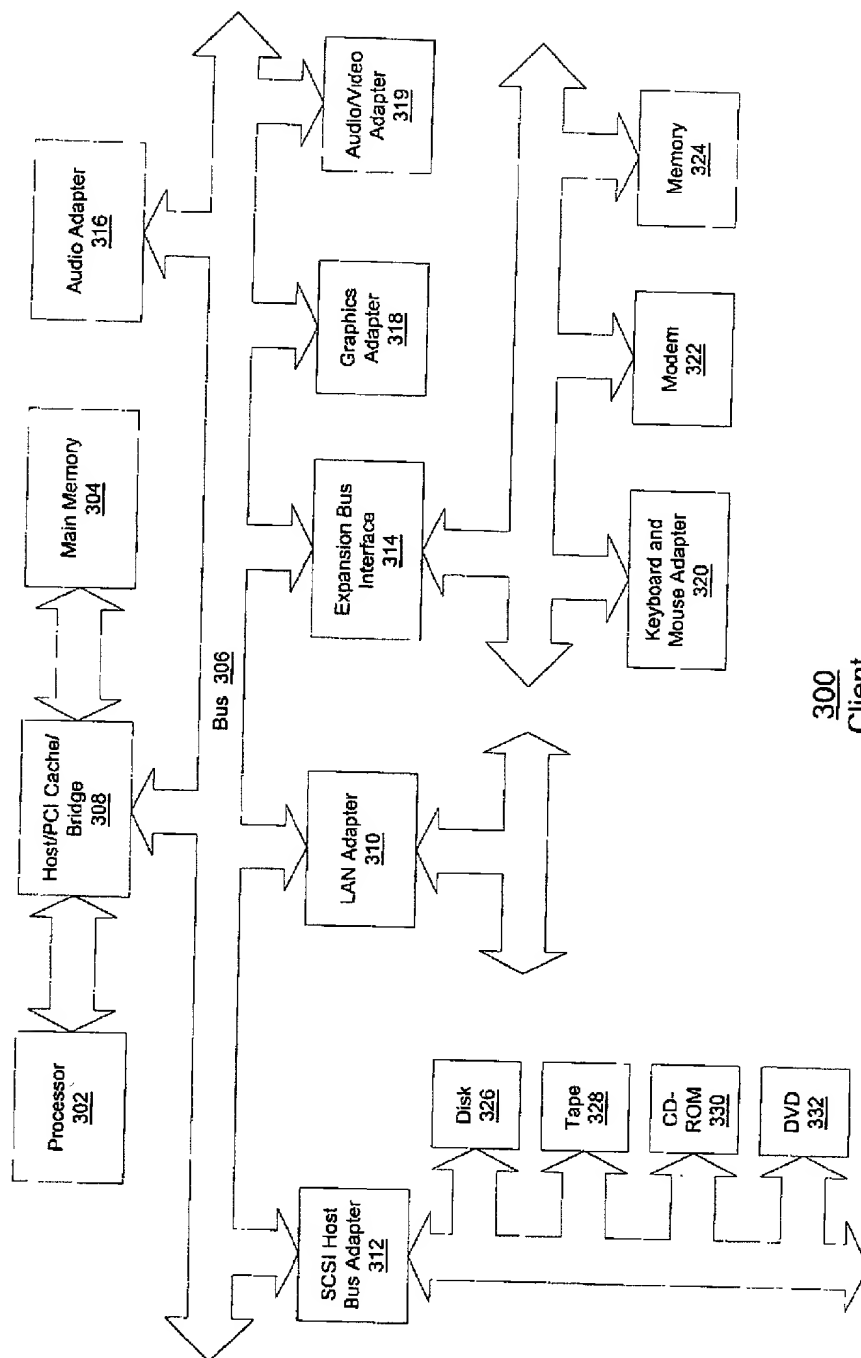


Figure 3
300
Client

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Figure 4

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